



**GOVERNMENT COLLEGE OF ENGINEERING JALGAON**  
 (An Autonomous Institute of Government of Maharashtra and affiliated to KBC North  
 Maharashtra University, Jalgaon)

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**Coursewise Marks of Theory Courses**

**Year and Programme :** IV Year B.TECH. (ELECTRONICS AND TELECOMMUNICATION ENGINEERING)

**Course Code and Course Name :** ET402UD - Very Large Scale Integration Design

**Examination :** Winter-2024

**Credit :** 3

**Semester :** EVEN

**Total Maximum Marks :** 100

**Academic Year :** 2024-25

| Sr. No. | PRN     | Name of Candidate          | MSE | ISE-II | ISA | ESE (Revaluation) |     |       |       | Total | Result (Pass /Fail) | Remark |
|---------|---------|----------------------------|-----|--------|-----|-------------------|-----|-------|-------|-------|---------------------|--------|
|         |         |                            |     |        |     | Before Reval      | Ist | IIInd | Final |       |                     |        |
| 1       | 2111030 | PATKOTWAR KARTIKEYA RAJESH | 6   |        | 7   | 21                | 23  | -     | 23    | 36    | FAIL                | Change |

**AEC Officer**

**Asst. CoE(Post Exam)**

**CoE**

**Autogenerated by MIS**

**Read By**

**Checked By**

**Approved By**

**Copy To**

1. Departmental Notice Board
2. L. Y. B. Tech. Coordinator/HoD
3. Dean Academic

Note : 'NA' - Not Applicable